

# Claims

- [c1] 1. A display panel with an integrated driver circuit, comprising:
- an array substrate;
  - a plurality of peripherals comprising a first peripheral area, a second peripheral area, a third peripheral area, and a fourth peripheral area;
  - a liquid crystal injection hole for injecting a liquid crystal, disposed on the fourth peripheral area of the array substrate;
  - a first driver circuit disposed on the adjacent edge of the first peripheral area and the second peripheral area of the array substrate;
  - a second driver circuit disposed on either the third peripheral area of the array substrate or disposed on the third peripheral area of the array substrate and part of the first peripheral area where the first driver circuit is not disposed on; and
  - a display area comprising a plurality of the data lines and a plurality of gate lines that are perpendicularly crossed with each other.
- [c2] 2. The display panel with the integrated driver circuit of

claim 1, wherein the first driver circuit is a data driver circuit, and the second driver circuit is a gate driver circuit.

[c3] 3. The display panel with the integrated driver circuit of claim 2, wherein the first driver circuit is the gate driver circuit, and the second driver circuit is the data driver circuit.

[c4] 4. The display panel with the integrated driver circuit of claim 2, wherein the data driver circuit is disposed outside of a range formed by the liquid crystal injection hole and the display area.

[c5] 5. The display panel with the integrated driver circuit of claim 2, wherein the data driver circuit comprises a plurality of data driver units, so as to drive the data lines corresponded to the display unit, respectively.

[c6] 6. The display panel with the integrated driver circuit of claim 2, wherein the gate driver circuit comprises driving the gate lines on the display unit, respectively.

[c7] 7. The display panel with the integrated driver circuit of claim 1, wherein the array substrate is a rectangle.

[c8] 8. The display panel with the integrated driver circuit of claim 1, wherein the first driver circuit is disposed on

part of the second peripheral area.

- [c9] 9. The display panel with the integrated driver circuit of claim 1, wherein the second driver circuit is disposed on part of the first peripheral area.
- [c10] 10. The display panel with the integrated driver circuit of claim 1, wherein the second driver circuit is disposed on part of the third peripheral area.
- [c11] 11. The display panel with the integrated driver circuit of claim 1, wherein the display area is an active matrix driving display area.
- [c12] 12. The display panel with the integrated driver circuit of claim 1, wherein the display panel is a LCD panel.
- [c13] 13. The display panel with the integrated driver circuit of claim 1, wherein the array substrate is an isolating substrate.